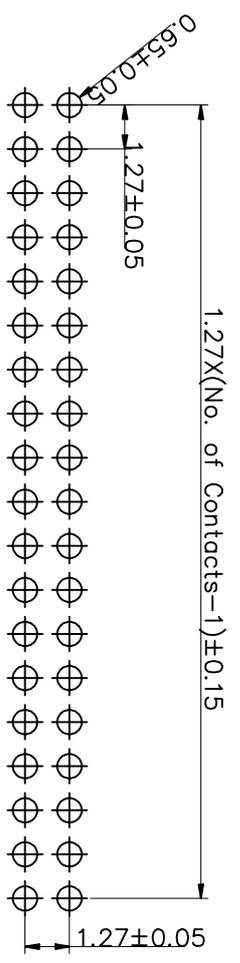
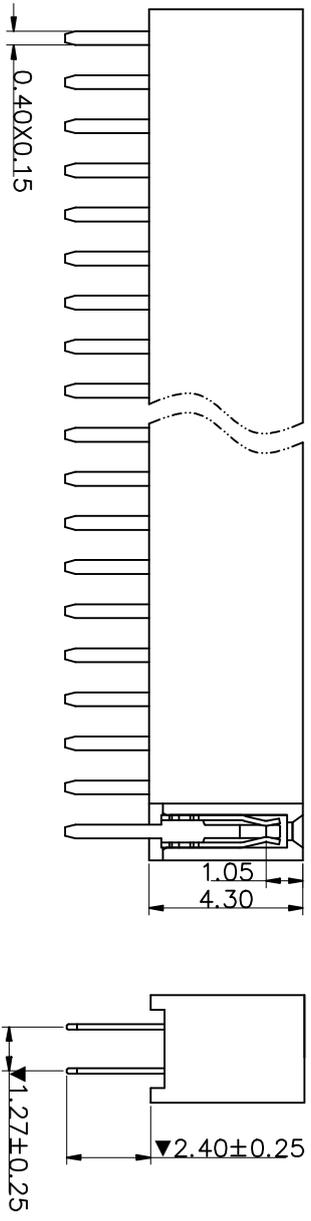
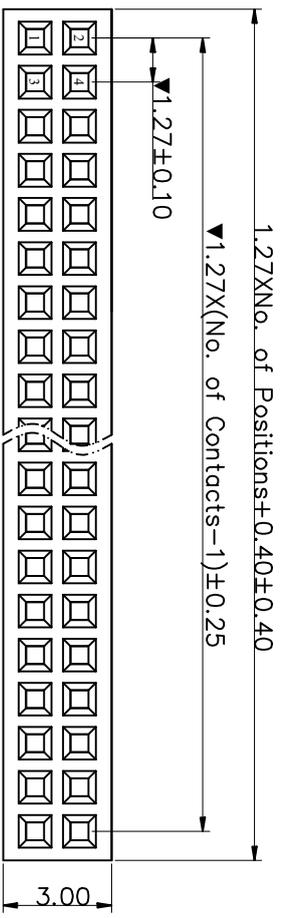


CUSTOMER COPY



REVISION DATA				
SYM	ECN	DESCRIPTION	DATE	APPROVED



Recommended PCB Board Hole Layout

Technical Data
 Contact: brass JIS C5191(Cu5Sn6)
 Contact plating: see ordering information
 Insulator: black glass filled polyester
 Current rating: UL94V-0
 Contact resistance: 20mΩ max.
 Insulation resistance: 1000MΩ min. at 100V
 Withstanding Voltage: 300V AC/minute
 Lead size pin: 0.40mm SQ
 Average insertion force: 100g/contact max.
 Average withdrawal force: 40g/contact min.
 Retention force: 0.35Kg/contact min.
 Mechanical life cycle: min.200
 Operating temperature: -55°C to +105°C
 Soldering temperature: 245°C±5 5S
 Critical Dimensions: ▼

FEA2 43 0 - U XX X X A 01

High Option
43=4.3mm

No. of Positions
03~80

Plating Option
A G/F
B Au3μ"
C Au5μ"
D Au10μ"
E Au15μ"
S Special

Insulator Material Option
A BK-PBT
B BK-PA9T
S Special
(Underline option is standard, include all insulators)

Lead Style
01~99

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 GUANGDONG NATCONN ELECTRONIC CO.,LTD

TOLERANCE: ANGLE ±2°	PROJECTION	TITLE: I, 27*1.27 Female Header Dual Row Straight H=4.3, DIP=2.4mm	REVISION: N/A
X.XXX ±0.01	DATE: 2018/05/17	CUSTOMER: N/A	REVISION: N/A
X.XX ±0.25	DRAWN: 王伟超	CUSTOMER P/N: N/A	REVISION: A
X.X ±0.35	CHECKED: 唐建平	PART NO.: N/A	
X. ±0.50	APPROVED: 冯显东	DRAWING NO.: FEA2430UA01	
		SIZE: A4	
		UNIT: mm	
		SCALE: 1:1	
		SHEET: 1/1	