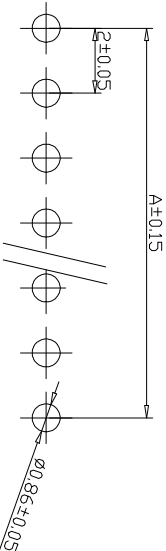
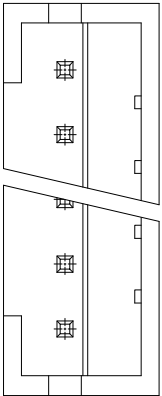
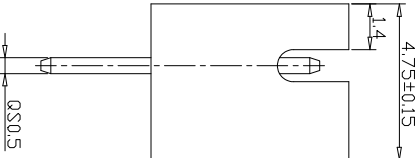
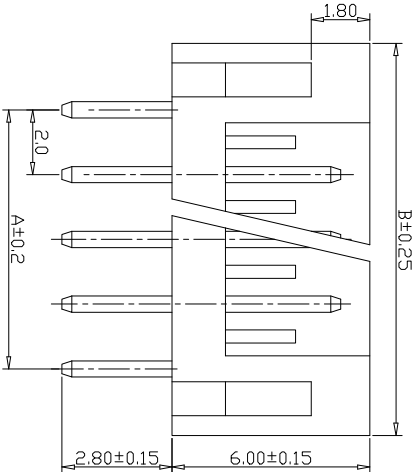


REVISION DATA			
SYM	ECN	DESCRIPTION	DATE

DATE	APPROVED



Recommended PCB Hole Layout



Dimensional & Ordering Information

poles	2-16 poles
Pitch between ploes	2.0mm
Rated Voltage	100V AC,DC
Rated current	2.0A
Withstand Voltage	800V.AC/minute
Insulation Resistance	≥1000 MΩ
Temperaure Range	-25℃~+105℃
Applicable Wire	AWG#24~#32

16	30.0	34.0
15	28.0	32.0
14	26.0	30.0
13	24.0	28.0
12	22.0	26.0
11	20.0	24.0
10	18.0	22.0
9	16.0	20.0
8	14.0	18.0
7	12.0	16.0
6	10.0	14.0
5	8.0	12.0
4	6.0	10.0
3	4.0	8.0
2	2.0	6.0
Circuits	A	B
	Dimension(mm)	

Packing		® 广东纳特康电子股份有限公司 GUANGDONG NATCONN ELECTRONIC CO.,LTD			
1.General	√				
2.Tape&Reel					
3.Tube					
4.Tray					
5.Other					

TOLERANCE:		PROJECTION		TITLE:	
ANGLE ±2°		DATE		Pitch 2.0mm Sq 0.5针座	
X.XXX ±0.01		DRAWN		CUSTOMER: N/A	
X.XX ±0.15		CHECKED		PART NO.: PH-020A01	
X.X ±0.25		APPROVED		SIZE: A4	
X. ±0.50				UNITS: mm	
				SCALE: 1:1	
				SHEET: 1/1	